

MURHB820CT - MURHB860CT

8.0A SURFACE MOUNT GLASS PASSIVATED DUAL SUPERFAST RECTIFIER

Features

- Fred Chip Planar Construction
- Ideally Suited for Automatic Assembly
- Super-Fast Recovery Time
- High Voltage Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in High Voltage, High Frequency Inverters, Free Wheeling, and Switching **Power Supplies**

Mechanical Data

Case: D²PAK/TO-263, Molded Plastic Terminals: Plated Leads Solderable per

MIL-STD-202, Method 208 Polarity: See Diagram

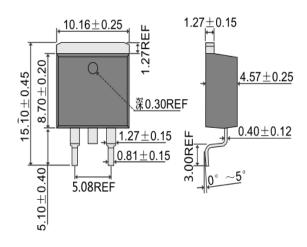
Weight: 1.7 grams (approx.)

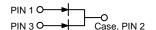
Mounting Position: Any

Marking: Device Code, See Page 3

Lead Free: For RoHS / Lead Free Version

D²PAK/TO-263





Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

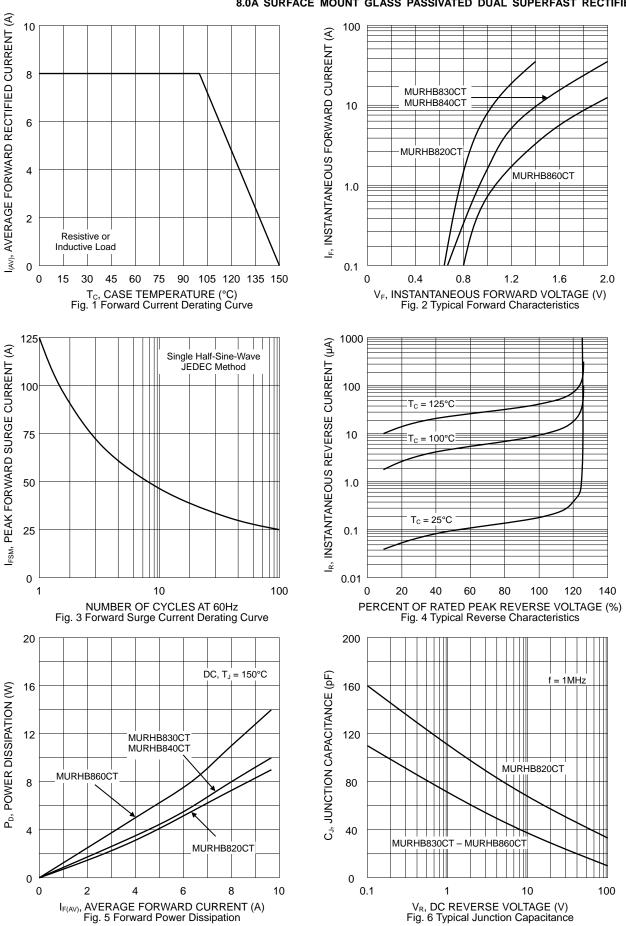
Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	MURHB 820CT	MURHB 830CT	MURHB 840CT	MURHB 860CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	200	300	400	600	\ \
RMS Reverse Voltage	VR(RMS)	140	210	280	420	٧
Average Rectified Output Current Total Device $@T_C = 100^{\circ}C$ Per Diode	lo	8.0 4.0				Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	125				А
Forward Voltage per diode @I _F = 4.0A	VFM	0.95	1	.3	1.7	V
	IRM	10 500				μΑ
Reverse Recovery Time (Note 1)	trr	35 50			nS	
Typical Junction Capacitance (Note 2)	Сл	85 50			pF	
Thermal Resistance Junction to Ambient per diode Thermal Resistance Junction to Case per diode	R JA R JC	60 3.0				°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150				°C

Note: 1. Measured with I_F = 0.5A, I_R = 1.0A, I_{RR} = 0.25A. 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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MARKING INFORMATION

RECOMMENDED FOOTPRINT

